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APPLICANTS Torayuki Tsukada, Kyoto, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/09292 07/22/2003				
** FOREIGN APPLICATIONS ***** JAPAN 2002-215746 07/24/2002 JAPAN 2002-215747 07/24/2002				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		STATE OR COUNTRY JAPAN	SHEETS DRAWING 24	TOTAL CLAIMS 20
Verified and Acknowledged Examiner's Signature _____ Initials _____		INDEPENDENT CLAIMS 4		
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TITLE CHIP RESISTOR AND MANUFACTURING METHOD THEREFOR				
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